

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 09/810,595  
Filing Date ..... March 15, 2001  
Inventorship ..... Belford T. Coursey  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Yennhu B. Huynh  
Attorney's Docket No. .... MI22-1660  
Title: Memory Circuitry With Plurality of Capacitors Received Within an Insulative Layer Well

#22  
1/14/04  
[Signature]

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References - See Attached Form PTO-1449

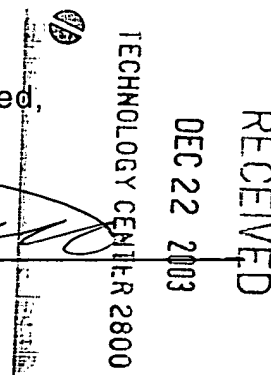
In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. Copies of the cited art are attached hereto. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first office action after the filing of a request for continued examination. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 12-15-03

By: [Signature]  
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